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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Hellerstein et al

Serial No.

10/789,123

Examiner:

Unassigned

Filed

February 27, 2004

Art Unit:

2191

For

METHODS AND ARRANGEMENTS FOR ORDERING

CHANGES IN COMPUTING SYSTEMS

December 27, 2006

## INFORMATION DISCLOSURE STATEMENT (IDS) UNDER 37 CFR 1.97

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

Applicants submit herewith patents, publications or other information which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. §1.56.

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Respectfully submitted,

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I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" Service under 37 C.F.R. § 1.10, "Express Mail" Mailing Label No. EV 736794805 US, Date of Deposit December 27, 2006, on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450.

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December 27, 2006

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Approved for use through 03/31/2007. OMB 0651-0031 DEC 27 2006 U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number. Substitute for for 1449/PTO Complete if Known **Application Number** 10/789,123 **INFORMATION DISCLOSURE** Filing Date February 27, 2004 STATEMENT BY APPLICANT **First Named Inventor** Joseph L. Hellerstein Art Unit 2191 (Use as many sheets as necessary) **Examiner Name** Unassigned Sheet Attorney Docket Number 1 590.125

of

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	а	J. Blazewicz, K. Ecker, G. Schmidt, J. Weglarz, Scheduling in Computer and Manufacturing Systems, Springer-Verlag, 1993	
	b	E. Coffman, editor, Computer and Job-Shop Scheduling Theory, John Wiley and Sons, 1976	
	С	M. Pindeo, Scheduling: Theory Algorithms and Systems, Prentice Hall 1995	
	đ	E. Lawler, Combinatorial Optimization: Networks and Matroids, Holt, Rinehart and Winston, 1976	
	е	W. Press, B. Flanner, S. Teukolsk, W. Vetterling, Numerical Recipes: The Art of Scientific Computing, Cambridge University Press, 1986	
	f	M. Garey, D. Johnson, Computers & Intractability: A Guide to the Theory of NP-Completeness, W. H. Freeman & Company, 1979	
	g	R. Motwani, P. Raghavan, Randomized Algorithms, Cambridge University Press, 1995	
	h	The Institute of Electrical and Electronics Engineers, Inc., 1996, IEEE Standard for Information Technology - Portable Operating System Interface (POSIX) System Administration	
		Part 2: Software Administration	
	i	CAE Specification, Systems Management: Distributed Software Administration (XDSA), The Open Group, January 1998	

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<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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